a memory circuit for storing said digital data from the digital signal source;

a playback circuit for reproducing said digital data stored in said memory circuit;

an inner battery; and

a battery switch,

wherein said battery switch enables to use power from said digital source having a higher operating voltage than that of said inner battery when said memory circuit stores said digital data in a condition of connecting to said digital source, and to use power from said inner battery when said playback circuit reproduces said digital data in a condition of being removed from said digital source.

56.(New) A memory apparatus according to Claim 55, wherein said digital data includes audio data and an identification (ID) code specifying a reproducing condition of said audio data, and said playback circuit reproduces said audio data following said reproducing condition.

57.(New) A memory apparatus according to Claim 55, wherein said ID code is inserted in the head of said digital data and is followed by said audio data, and said ID code and said audio data are integrally stored in said memory circuit.

58.(New) A memory apparatus according to Claim 55, comprising a data transfer circuit to operate at an increased data transfer speed at said higher operating voltage from said digital source, in comparison to a data transfer speed at an operating voltage of said inner battery.